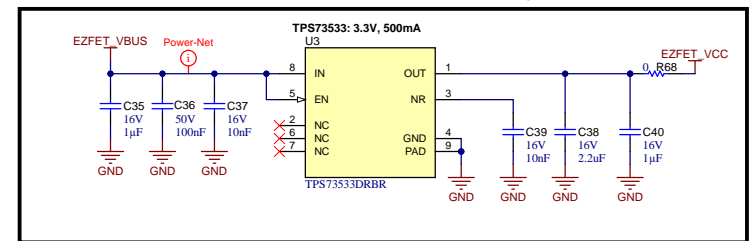
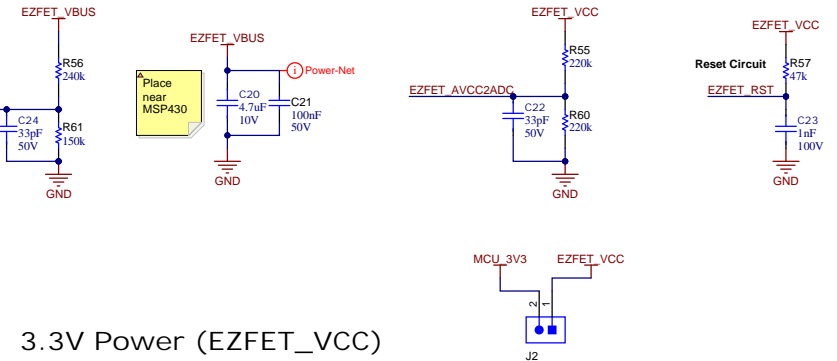
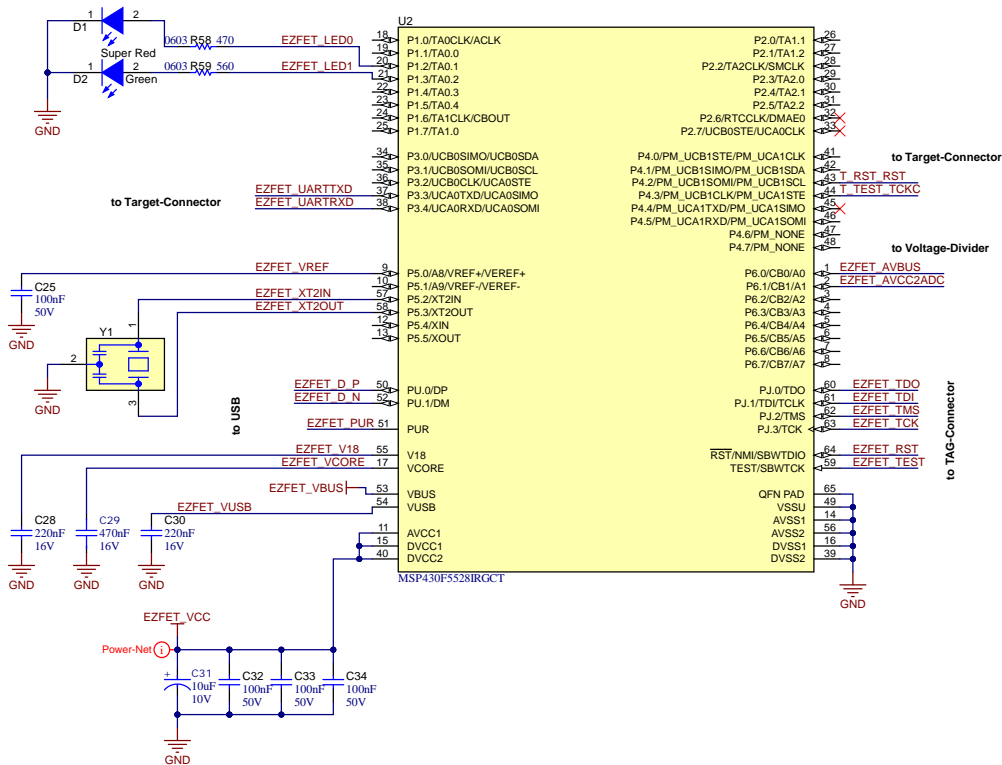
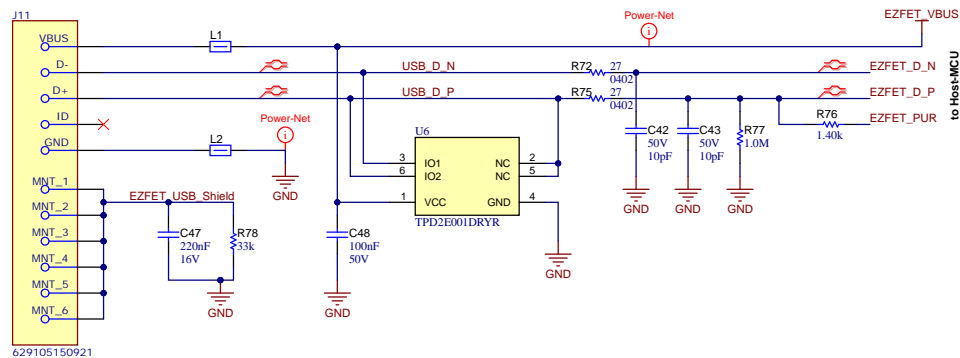


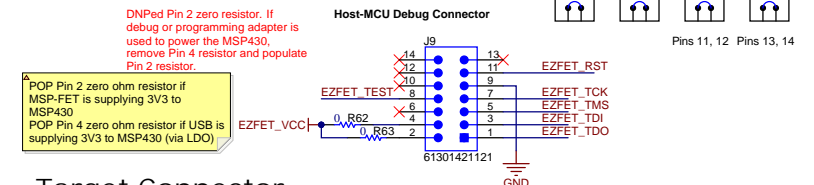
Host MCU for Emulation



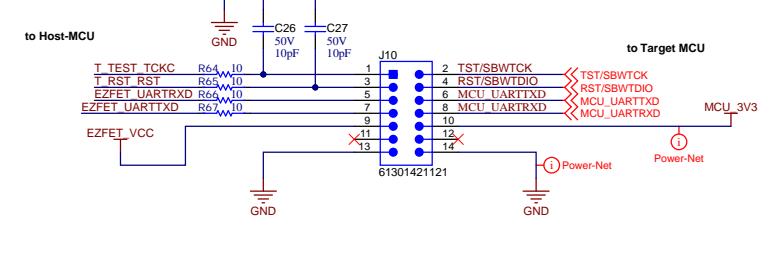
USB-I nterface



JTAG-Connector (Host Debug)



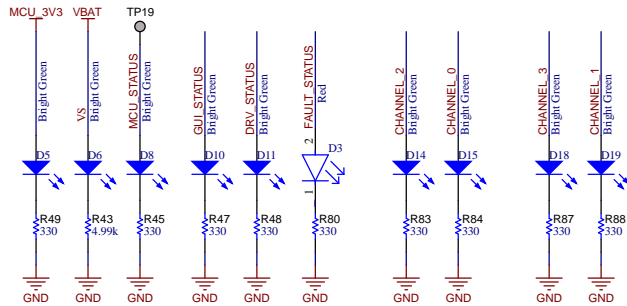
Target Connector



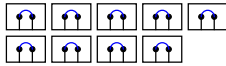
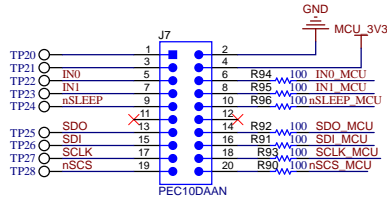
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Orderable:	Designed for: Public Release	Mod. Date: 17-04-2024
TID #: N/A	Project Title: MD086	
Number: MD-086	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 1 of 3
Drawn By: Murugavel Raju	File: MD086E1_ezFET_SchDoc	Size: B
Engineer: Sachin S	Contact: http://www.ti.com/support	http://www.ti.com

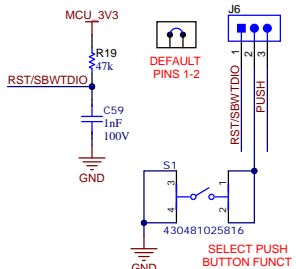
LEDS



Main Signal Header



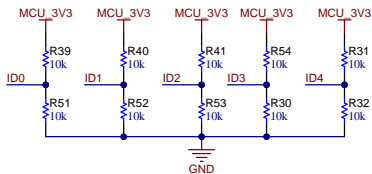
RST/PUSH Button



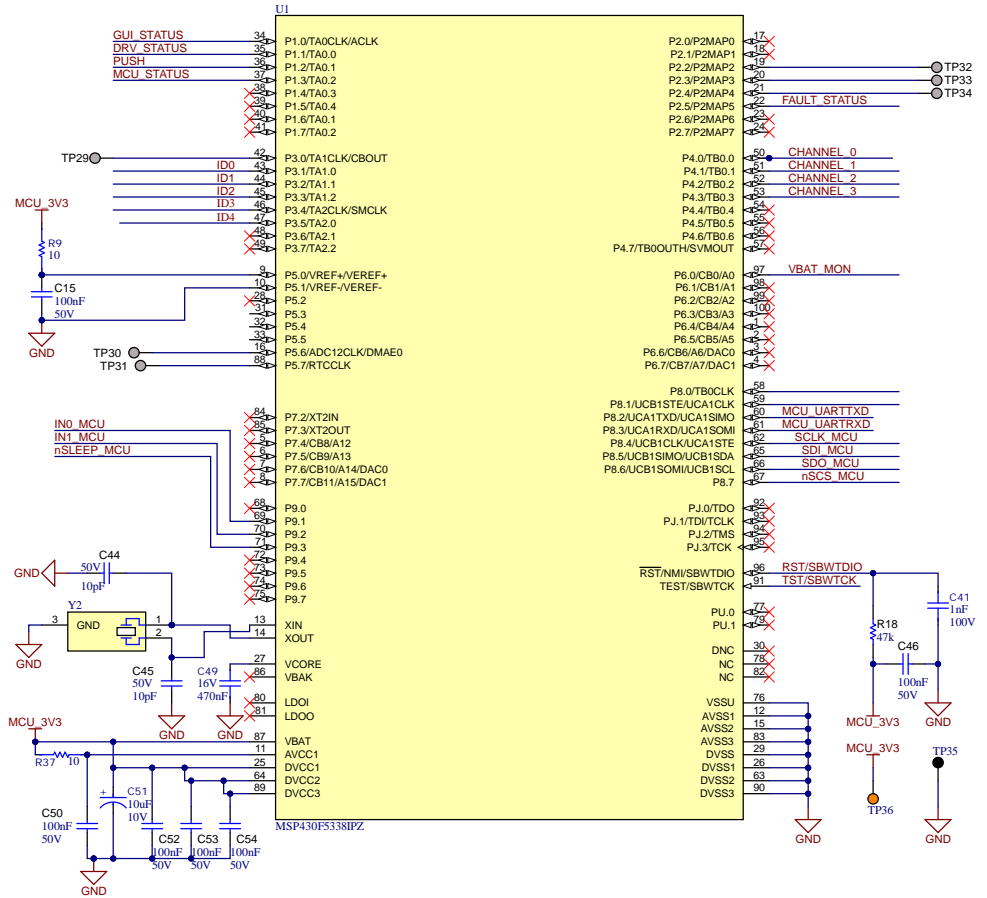
ID

The resistors on the ID[2:0] nets inform the firmware which device ID variant is on this board

Device	ID1	Type	ID3
DRV81008-Q1	0	Prerelease	0
DRV81004-Q1	1	Release	1
Just In Case	2		
Revision 1	3		
Revision 2	4		



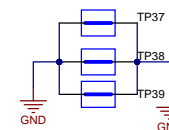
MSP430













Orderable:	Designed for: Public Release	Mod. Date: 18-04-2024
TID #: N/A	Project Title: MD086	
Number: MD-086	Rev: E1	Sheet Variant:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet 2 of 3
Drawn By: Tilden Chen	File: MD086E1_MCU_SchDoc	Size: B
Engineer: Sachin S	Contact: http://www.ti.com/support	

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The schematic diagram illustrates the DRV81004HTSSOP motor driver module. It features a power supply section with a 100V capacitor (C1) and a 10uF capacitor (C2) connected to VM and VDD_3V3. A 0.068uF capacitor (C9) is connected to the VDD pin. A resistor (R1) is connected between the VDD pin and the VM pin. The module is labeled DRV81004HTSSOP. The output pins are labeled OUT0 LS, OUT1 LS, OUT2 LS, and OUT3 LS. The module is connected to a motor, which is represented by a blue square symbol with a circle inside, labeled J1. The motor is connected to the output pins OUT0 LS, OUT1 LS, OUT2 LS, and OUT3 LS. The module is also connected to a ground (GND) pin. The module is labeled Thermal_PAD.



Orderable:	Designed for: Public Release	Mod. Date: 24-04-2024
TID #: N/A	Project Title: MD086	
Number: MD-086	Rev: E1	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By: Tilden Chen	File: MD086E1_DRV81xxx.SchDoc	Size: B
Engineer: Sachin S	Contact: http://www.ti.com/support	

1	2	3	4																			
<div>H1  SJ-5303 (CLEAR)</div> <div>FID1 </div> <div>PCB LOGO Texas Instruments</div>	<div>H2  SJ-5303 (CLEAR)</div> <div>FID2 </div> <div>CE Mark </div>	<div>H3  SJ-5303 (CLEAR)</div> <div>FID3 </div> <div>PCB LOGO FCC disclaimer</div> <div>PCB LOGO WEEE logo</div> <div> CAUTION HOT SURFACE</div> <div> CAUTION HOT SURFACE</div>	<div>H4  SJ-5303 (CLEAR)</div> <div>PCB LOGO CAUTION. READ USER GUIDE BEFORE USE</div>																			
A			A																			
B			B																			
C	<div>LBL1 <div>PCB Label</div><div>THT-14-423-10</div><div>Size: 0.65" x 0.20 "</div></div> <div>ZZ1 <div>Label Assembly Note</div><div>This Assembly Note is for PCB labels only</div></div> <div>ZZ2 <div>Assembly Note</div><div>These assemblies are ESD sensitive, ESD precautions shall be observed.</div></div> <div>ZZ3 <div>Assembly Note</div><div>These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.</div></div> <div>ZZ4 <div>Assembly Note</div><div>These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.</div></div>	<div>Variant/Label Table</div> <table><thead><tr><th>Variant</th><th>Label Text</th></tr></thead><tbody><tr><td>001</td><td>DRV81004-Q1EVM</td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr></tbody></table>	Variant	Label Text	001	DRV81004-Q1EVM															<div>LBL2 <div>PCB Label</div><div>EVM Folder</div></div>	C
Variant	Label Text																					
001	DRV81004-Q1EVM																					
D		<div>Title</div> <table><tr><td>Size</td><td>Number</td><td>Revision</td></tr><tr><td>A</td><td></td><td></td></tr><tr><td>Date:</td><td>5-13-2024</td><td>Sheet of</td></tr><tr><td>File:</td><td colspan="2">D:\prakash\..\MD086E1_Hardware.SchDocDrawn By:</td></tr></table>	Size	Number	Revision	A			Date:	5-13-2024	Sheet of	File:	D:\prakash\..\MD086E1_Hardware.SchDocDrawn By:		D							
Size	Number	Revision																				
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File:	D:\prakash\..\MD086E1_Hardware.SchDocDrawn By:																					
1	2	3	4																			